



Europäisches Patentamt
European Patent Office
Office européen des brevets



EP 0 836 218 A2

(12) EUROPEAN PATENT APPLICATION

(43) Date of publication:
15.04.1998 Bulletin 1998/16

(51) Int Cl.⁶ **H01J 37/34**, **H01J 37/32**

(21) Application number: 97307864.5

(22) Date of filing: 06.10.1997

(84) Designated Contracting States:
AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC
NL PT SE
Designated Extension States:
AL LT LV RO SI

(72) Inventors:

- Edelstein, Sergio
Los Gatos, California 95030 (US)
- Subramani, Mani
San Jose, California 95135 (US)

(30) Priority: 08.10.1996 US 730722

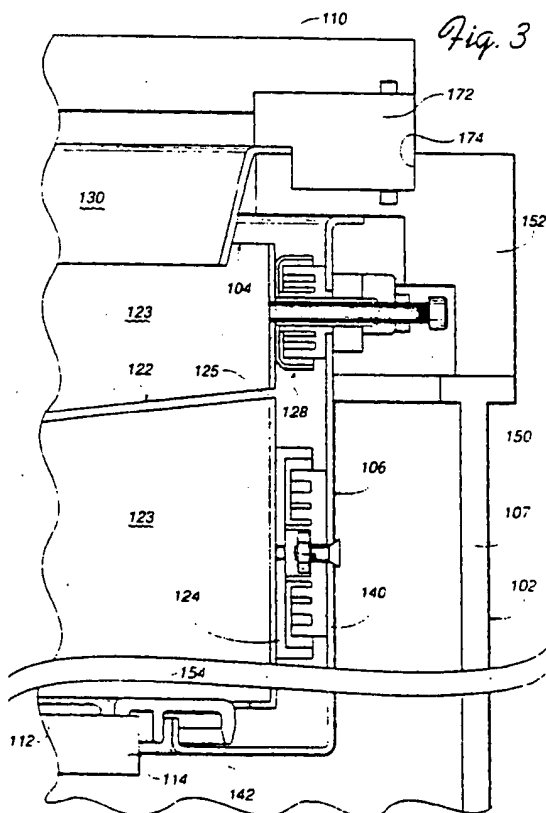
(74) Representative: **Bayliss, Geoffrey Cyril et al**
BOULT WADE TENNANT,
27 Furnival Street
London EC4A 1PQ (GB)

(71) Applicant: **APPLIED MATERIALS, INC.**
Santa Clara, California 95054-3299 (US)

(54) **Active shield for generating a plasma for sputtering**

(57) A combination coil and shield (104) for a plasma chamber (100) in a semiconductor fabrication system is provided. The coil-shield has a plurality of turns

to couple energy efficiently into a plasma and also substantially blocks deposition material from reaching a second shield (106) positioned behind the first shield



EP 0 836 218 A2

Description

The present invention relates to plasma generators, and more particularly, to a method and apparatus for generating a plasma to sputter deposit a layer of material in the fabrication of semiconductor devices.

Low pressure radio frequency (RF) generated plasmas have become convenient sources of energetic ions and activated atoms which can be employed in a variety of semiconductor device fabrication processes including surface treatments, depositions, and etching processes. For example, to deposit materials onto a semiconductor wafer using a sputter deposition process, a plasma is produced in the vicinity of a sputter target material which is negatively biased. Ions created adjacent to the target impact the surface of the target to dislodge, i.e., "sputter" material from the target. The sputtered materials are then transported and deposited on the surface of the semiconductor wafer.

Sputtered material has a tendency to travel in straight line paths from the target to the substrate being deposited, at angles which are oblique to the surface of the substrate. As a consequence, materials deposited in etched trenches and holes of semiconductor devices having trenches or holes with a high depth to width aspect ratio, can bridge over causing undesirable cavities in the deposition layer. To prevent such cavities, the sputtered material can be redirected into substantially vertical paths between the target and the substrate by negatively charging the substrate to position vertically oriented electric fields adjacent the substrate if the sputtered material is sufficiently ionized by the plasma. However, material sputtered in a low density plasma often has an ionization degree of less than 1% which is usually insufficient to avoid the formation of an excessive number of cavities. Accordingly, it is desirable to increase the density of the plasma to increase the ionization rate of the sputtered material in order to decrease the formation of unwanted cavities in the deposition layer. As used herein, the term "dense plasma" is intended to refer to one that has a high electron and ion density.

There are several known techniques for exciting a plasma with RF fields including capacitive coupling, inductive coupling and wave heating. In a standard inductively coupled plasma (ICP) generator, RF current passing through a coil surrounding the plasma induces electromagnetic currents in the plasma. These currents heat the conducting plasma by ohmic heating so that it is sustained in steady state. As shown in U.S. Pat. No. 4,362,632, for example, current through a coil is supplied by an RF generator coupled to the coil through an impedance matching network, such that the coil acts as the first windings of a transformer. The plasma acts as a single turn second winding of a transformer.

In a number of deposition chambers such as a physical vapor deposition chamber, the chamber walls are often formed of a conductive metal such as stainless steel. Because of the conductivity of the chamber walls,

it is often necessary to place the antenna coils or electrodes within the chamber itself because the conducting chamber walls would block or substantially attenuate the electromagnetic energy radiating from the antenna. As a result, the coil and its supporting structures are directly exposed to the deposition flux and energetic plasma particles. This is a potential source of contamination of the film deposited on the wafer, and is undesirable.

To protect the coils, shields made from nonconducting materials, such as ceramics, can be placed in front of the coil. However, many deposition processes involve deposition of conductive materials such as aluminum on the electronic device being fabricated. Because the conductive material will coat the ceramic shield, it will soon become conducting, thus again substantially attenuating penetration of electromagnetic radiation into the plasma.

It is an object of the present invention to provide an improved method and apparatus for generating plasmas within a chamber, obviating, for practical purposes, the above-mentioned limitations.

These and other objects and advantages are achieved by, in accordance with one aspect of the invention, a plasma generating apparatus which comprises a conductive shield which forms a multi-turn coil. The conductive coil-shield is coupled to an RF source such that the coil-shield inductively couples electromagnetic energy to a plasma. Such an arrangement is believed to avoid attenuation of the RF power while at the same time substantially reducing the generation of contaminating particles from the coil-shield. Furthermore, because the coil-shield has a plurality of turns, the current required to radiate the RF power is reduced as compared to that required of single turn coils.

The following is a description of some specific embodiments of the invention, reference being made to the accompanying drawings, in which:

Fig. 1 is a perspective, partial cross-sectional view of a plasma generating chamber in accordance with one embodiment of the present invention.

Fig. 2 is a schematic diagram of the electrical interconnections to the plasma generating chamber of Fig. 1.

Fig. 3 is a schematic partial cross-sectional view of the plasma generating chamber of Fig. 1 shown installed in a vacuum chamber.

Fig. 4 is a schematic cross-sectional view of a plasma generating chamber in accordance with an alternative embodiment of the present invention.

Referring first to Figs. 1 and 2, a plasma generator in accordance with an embodiment of the present invention comprises a substantially cylindrical plasma chamber 100 which is received in a vacuum chamber 102 (shown schematically in Fig. 2). The plasma chamber 100 of this embodiment has an inner shield 104 which is formed into a multi-turn coil and is insulatively carried internally by an outer shield 106. The shields 104 and 106 protect the interior walls 107 (Fig. 3) of the vacuum chamber 102 from the material being deposited within

the interior of the plasma chamber 100.

Radio frequency (RF) energy from an RF generator 108 is radiated from the coil-shield 104 into the interior of the deposition system 100, which energizes a plasma within the deposition system 100. An ion flux strikes a negatively biased target 110 positioned at the top of the chamber 102. The target 110 is negatively biased by a DC power source 111. The ions eject material from the target 110 onto a substrate 112 which may be a wafer or other workpiece which is supported by a pedestal 114 at the bottom of the deposition system 100. A rotating magnetron magnet assembly 116 provided above the target 110 produces magnetic fields which sweep over the face of the target 110 to promote uniform erosion of the target.

The atoms of material ejected from the target 110 are in turn ionized by the plasma being energized by the coil-shield 104 which is inductively coupled to the plasma. The RF generator 108 is preferably coupled to the coil-shield 104 through an amplifier and impedance matching network 118. The other end of the coil-shield 104 is coupled to ground, preferably through a capacitor 120 which may be a variable capacitor. The ionized deposition material is attracted to the substrate 112 and forms a deposition layer thereon. The pedestal 114 may be negatively biased by an AC (or DC or RF) source 121 so as to externally bias the substrate 112. As set forth in greater detail in copending application Serial No. 08/677,588, filed July 9, 1996 (Attorney Docket No. 1402/PVD/DV), entitled "Method for Providing Full-Face High Density Plasma Deposition" by Ken Ngan, Simon Hui and Gongda Yao, which is assigned to the assignee of the present application and is incorporated herein by reference in its entirety, external biasing of the substrate 112 may optionally be eliminated.

The coil-shield 104, like the coil-shield described in EP-A-0774986 protects the vacuum chamber walls 107 from the material being deposited. However, as will be explained in greater detail below, in accordance with one aspect of the present invention, the coil-shield 104 has a helix-shaped design which permits the coil-shield 104 to function as a multi-turn coil. As a result, the amount of current necessary to couple RF energy into the plasma is reduced as compared to that required by a single turn coil. By reducing the current carrying requirements of the chamber components, the design can be simplified and costs reduced.

In the illustrated embodiment, the coil-shield 104 is made of a conductive material such as heavy duty bead blasted solid high-purity (preferably 99.995% pure) titanium formed into a generally cylindrical shape having a diameter of 10-12 inches. However, other highly conductive materials may be utilized depending upon the material being sputtered and other factors. For example, if the material to be sputtered is aluminum, both the target and the coil-shield 104 may be made of high purity aluminum.

To enable the coil-shield 104 to function as a multi-

turn coil, a thin channel 122 (preferably 1/4 to 1/8 inches in width) cut completely through the wall of the shield 104 is formed in a helix shape so that the resultant coil-shield 104 comprises a continuous strip of conductive material wound in a helix. Each turn or winding 123 of the coil-shield 104 is separated from the adjacent turn by a gap 125 (Fig. 3) provided by the continuous channel 122. The overall shape of the coil-shield 104 of the illustrated embodiment is generally cylindrical but other shapes may be used depending upon the application.

In the illustrated embodiment, the channel 122 is cut through the shield 104 by use of a laser. Alternatively other cutting methods may be used such as EDM which uses a hot thin wire to cut metal. Although a helix shape for the channel 122 is preferred to simplify laser cutting methods, it is anticipated that the channel may be cut in other shapes including a rectilinear shape as represented by the channel 122a (Fig. 4). The channel 122a comprises straight parallel segments 400 and 402 to facilitate cutting by rotary blades and other cutting implements. In yet another embodiment, the coil-shield may be formed from a flat strip of material which is rolled into a generally cylindrical, multi-turn shape.

The channel 122 encircles the cylinder-shaped coil-shield 104 approximately three times in the illustrated embodiment to define approximately three windings or turns of the coil. It should be appreciated that the particular number of windings may vary, depending upon the particular application. Increasing the number of windings will generally reduce the necessary current levels to couple a given amount of power into the plasma but the costs of fabricating the coil-shield may be increased by cutting additional windings into the shield.

The coil-shield 104 is carried on the outer shield 106 by a plurality of coil standoffs 124 (Fig. 3) which electrically insulate the coil-shield 104 from the supporting shield 106. As set forth in greater detail in copending application Serial No. 08/647,182, entitled "Recessed Coil for Generating a Plasma," filed May 9, 1996 (Attorney Docket # 1186/PVD/DV) and assigned to the assignee of the present application, which application is incorporated herein by reference in its entirety, the insulating coil standoffs 124 have an internal labyrinth structure which permits repeated deposition of conductive materials from the target 110 onto the coil standoff 124 while preventing the formation of a complete conducting path of deposited material from the inner coil-shield 104 to the outer shield 106 which could short the coil 104 to the shield 106 (which is typically at ground).

RF power is applied to the coil 104 by feedthroughs 126 which are supported by insulating feedthrough standoffs 128. As represented in Fig. 2, the feedthroughs 126 are preferably positioned at the ends 127 of the coil-shield 104. The feedthrough standoffs 128, like the coil support standoffs 124, permit repeated deposition of conductive material from the target onto the feedthrough standoff 128 without the formation of a conducting path which could short the coil 104 to the

shield 106. Thus, the coil feedthrough standoff 128 has an internal labyrinth structure somewhat similar to that of the coil standoff 124 to prevent the formation of a short between the coil 104 and the wall 140 of the outer shield 106.

Another advantage of the illustrated embodiment, is that the inner coil-shield 104 protects the outer shield 106 from the material being deposited. As best seen in Fig. 3, the inner coil-shield 104 is positioned to substantially block material from reaching the outer shield 106 except for that material which passes through the channel 122 between adjacent turns of the inner coil-shield 106. Since the width of the channel 122 is relatively small as represented by the gap 125, the channel preferably occupies less than 10% of the overall interior surface area of the coil-shield 104. Thus, the coil-shield 104 encloses over 50% (preferably over 90%) of the lateral face of the plasma generation area between the target 110 and the substrate 112 and clamp 154. Consequently, only a relatively small amount of deposition material reaches the outer shield 106. Thus, conductive deposition material being deposited on the standoffs 124, 128 between the inner and outer shields can be substantially reduced. Because less material is deposited on the standoffs, it is anticipated that the design of the standoffs might be substantially simplified over the relatively complex labyrinth designs depicted in Fig. 3. For example, the labyrinth structure may have fewer channels.

Fig. 4 shows an alternative design in which the standoffs have been even further simplified. As shown therein, the coil support standoffs 124a are simple posts made of an insulative material such as ceramic. The feedthrough standoffs 128a (represented in phantom) similarly comprise a hollow ceramic post through which the RF feedthrough passes. In some applications, the amount of conductive deposition material passing through the channel 122a of the inner coil-shield 104a may be so reduced as to substantially eliminate the chance of a conductive path being deposited on the standoffs 124a or 128a which could short the inner coil-shield 104 to the outer shield 106.

In addition to reducing the amount of deposition material reaching the support and feedthrough standoffs, the inner coil-shield 104 conversely reduces or eliminates particulate deposition matter shed from the standoffs from reaching the substrate 112. Because of their relative sharp curves, the standoffs are believed to inherently shed more particulate matter than relatively flat surfaces such as the shields 104 and 106. However, because the standoffs are placed between the inner coil-shield 104 and the outer shield 106, and because the width of the channel 122 of the coil-shield 104 is small, it is believed that the coil-shield 104 substantially blocks any such particulate matter from the standoffs from reaching the interior of the plasma chamber and the substrate.

As best seen in Figs. 1 and 3, the plasma chamber 100 has a dark space shield ring 130 which provides a

ground plane with respect to the target 110 above which is negatively biased. In addition, as explained in greater detail in the aforementioned copending application Serial No. 08/647,132, the shield ring 130 shields the outer edges of the target from the plasma to reduce sputtering of the target outer edges. The dark space shield 130 performs yet another function in that it is positioned to shield the coil-shield 104 from the material being sputtered from the target 110. The dark space shield 130 does not completely shield the coil-shield 104 from all of the material being sputtered since some of the sputtered material travels at an oblique angle with respect to the vertical axis of the plasma chamber 100. However, because much of the sputtered material does travel parallel to the vertical axis of the chamber or at relatively small oblique angles relative to the vertical axis, the dark space shield 130 which is positioned in an overlapping fashion above the coil-shield 104, prevents a substantial amount of sputtered material from being deposited on the coil-shield 104. By reducing the amount of material that would otherwise be deposited on the coil-shield 104, the generation of particles by the material which is deposited on the coil-shield 104 can be substantially reduced.

In the illustrated embodiment, the dark space shield 130 is a generally continuous ring of titanium (where titanium deposition is occurring in the chamber 100) or stainless steel having a generally inverted frusto-conical shape. The dark space shield extends inward toward the center of plasma chamber 100 so as to overlap the coil-shield 104 by a distance of 1/4 inch. It is recognized, of course, that the amount of overlap can be varied depending upon the relative size and placement of the coil and other factors. For example, the overlap may be increased to increase the shielding of the coil-shield 104 from the sputtered material but increasing the overlap could also further shield the target from the plasma which may be undesirable in some applications. Also, the dark space shield ring may have a slot to reduce eddy currents induced in the ring by the overlapping coil-shield 104.

The chamber shield 106 is generally bowl-shaped (Fig. 3) and includes a generally cylindrically shaped, vertically oriented wall 140 to which the standoffs 124 and 123 are attached to insulatively support the coil-shield 104. The shield further has a generally annular-shaped floor wall 142 which surrounds the chuck or pedestal 114 which supports the workpiece 112 which has an 8" diameter in the illustrated embodiment. A clamp ring 154 may be used to clamp the water to the chuck 114 and cover the gap between the floor wall of the shield 106 and the chuck 114.

The plasma chamber 100 is supported by an adapter ring assembly 152 which engages the vacuum chamber. The chamber shield 106 is grounded to the system ground through the adapter ring assembly 152. The dark space shield 130, like the chamber shield 106, is grounded through the adapter ring assembly 152.

The target 110 is generally disk-shaped and is also supported by the adapter ring assembly 152. However, the target 110 is negatively biased and therefore should be insulated from the adapter ring assembly 152 which is at ground. Accordingly, seated in a circular channel formed in the underside of the target 110 is a ceramic insulation ring assembly 172 which is also seated in a corresponding channel 174 in the upper side of the adapter ring assembly 152. The insulator ring assembly 172 which may be made of a variety of insulative materials including ceramics spaces the target 110 from the adapter ring assembly 152 so that the target 110 may be adequately negatively biased. The target, adapter and ceramic ring assembly are provided with O-ring sealing surfaces (not shown) to provide a vacuum tight assembly from the vacuum chamber to the target 110.

To promote uniform erosion of the target 110, a magnetron 116 (Fig. 2) may be provided above the target 110. However, the magnetron may be omitted by increasing the RF ionization of the plasma.

In order to accommodate the coil-shield 104 to facilitate ionization of the plasma, it has been found beneficial to space the target 110 from the surface of the workpiece 112. However, this increased spacing between the target and the workpiece can adversely impact the uniformity of the material being deposited from the target. Such nonuniformity typically exhibits itself as a thickening of the deposited material toward the center of the workpiece with a consequent thinning of the deposited material toward the edges of the workpiece. As set forth in copending application Serial No. 08/680,335, filed July 10, 1996 (Atty. Docket 1390CIP/PVD/DV) incorporated herein by reference, this nonuniformity may be effectively compensated by sputtering deposition material not only from the sputter target 110 above the workpiece but also from a coil or other secondary target encircling the edges of the workpiece. Here, because the edges of the workpiece are closer to the coil-shield 104 than is the center of the workpiece, any material sputtered from the coil-shield 104 may tend to deposit more thickly toward the edges of the workpiece than the center. This is generally the reverse of the deposition pattern of material from the target 110. By appropriately adjusting the ratio of RF power level applied to the coil-shield 104 to the DC power level of the bias applied to the target, the deposition level of the material being sputtered from the coil-shield 104 may be selected in such a manner as to compensate substantially for the nonuniformity of the deposition profile of the material from the target such that the overall deposition profile of the layer from both sources of sputtered can be substantially more uniform than that which has often been obtained from the overhead target alone.

It is presently believed that the amount of sputtering which will originate from the coil-shield 104 as compared to the sputtering which originates from the target 110 may be a function of the RF power applied to the coil-shield 104 relative to the DC power applied to the target

110. By adjusting the ratio of the coil RF power to the target DC power, the relative amounts of material sputtered from the coil-shield 104 and the target 110 may be varied so as to achieve the desired uniformity. It is believed that a particular ratio of the coil RF power to the target DC power will achieve the smallest degree of non-uniformity of the layer of material deposited from both the coil and the target. As the RF power to the coil is increased relative to the DC power applied to the target, the deposited layer tends to be more edge thick. Conversely, by decreasing the ratio of the RF power to the coil relative to the DC power applied to the target, the center of the deposited layer tends to grow increasingly thicker relative to the edges. Thus, by adjusting the ratio of the RF power to the coil relative to the DC power biasing the target, the material being sputtered from the coil can be increased or decreased as appropriate to effectively compensate for non-uniformity of the material being deposited from the target to achieve a more uniform deposited layer comprising material from both the target and the coil. For the multi-turn coil-shield 104, a coil RF power to target DC power ratio of approximately .7 may provide satisfactory results on an 8 inch diameter wafer.

It is further believed that the relative amounts of sputtering between the coil and the target may also be a function of the DC biasing of the coil-shield 104 relative to that of the target 110. This DC biasing of the coil-shield 104 may be adjusted in a variety of methods. For example, the matching network 302 typically includes inductors and capacitors. By varying the capacitance of one or more capacitors of the matching network, the DC biasing of the coil-shield 104 might be adjusted to achieve the desired level of uniformity. In one embodiment, the RF power to the coil and the DC biasing of the coil-shield 104 may have separate adjustment inputs to achieve the desired results. An alternative power arrangement could include two RF generators operated at slightly different frequencies. The output of one generator would be coupled to the coil in the conventional manner but the other generator at the slightly different frequency would be capacitively coupled to the coil such that a change in the power level of the second generator would change the DC bias of the coil. Such an arrangement could provide independent control of the RF power and DC bias applied to the coil. At present, it is believed that relatively large changes in DC bias to the coil for a given RF power level would be necessary to have a substantial effect on the amount of material sputtered from the coil.

Each of the embodiments discussed above utilized a single coil in the plasma chamber. It should be recognized that the present invention is applicable to plasma chambers having more than one RF powered coil. For example, the present invention may be applied to multiple coil chambers for launching helicon waves of the type described in EP-A-0774886.

The appropriate RF generators and matching cir-

cuits are components well known to those skilled in the art. For example, an RF generator such as the ENI Genesis series which has the capability to "frequency hunt" for the best frequency match with the matching circuit and antenna is suitable. The frequency of the generator for generating the RF power to the coil-shield 104 is preferably 2 MHz but it is anticipated that the range can vary from, for example, 1 MHz to 100 MHz. An RF power setting of 4.5 kW is preferred but a range of 1.5-5 kW is believed to be satisfactory. In some applications, energy may also be transferred to the plasma by applying AC or DC power to coils and other energy transfer members. A DC power setting for biasing the target 110 of 3 kW is preferred but a range of 2-5 kW and a pedestal bias voltage of -30 volts DC is believed to be satisfactory for many applications.

In the illustrated embodiment, the shield 106 has a diameter of 13 1/2" but it is anticipated that good results can be obtained so long as the shield has a diameter sufficient to extend beyond the outer diameter of the target, the substrate support and substrate, to shield the chamber from the plasma. The shield 106 may be fabricated from a variety of materials including insulative materials such as ceramics or quartz. However, the shield and all metal surfaces likely to be coated with the target material are preferably made of the same material as the sputtered target material but may be made of a material such as stainless steel or copper. The material of the structure which will be coated should have a coefficient of thermal expansion which closely matches that of the material being sputtered to reduce flaking of sputtered material from the shield or other structure onto the wafer. In addition, the material to be coated should have good adhesion to the sputtered material. Thus for example if the deposited material is titanium, the preferred metal of the shields, brackets and other structures likely to be coated is bead blasted titanium. Any surfaces which are likely to sputter would preferably be made of the same type of material as the target such as high purity titanium, for example. Of course, if the material to be deposited is a material other than titanium, the preferred metal is the deposited material, stainless steel or copper. Adherence can also be improved by coating the structures with molybdenum prior to sputtering the target. However, it is preferred that the coil (or any other surface likely to sputter) not be coated with molybdenum or other materials since the molybdenum can contaminate the workpiece if sputtered from the coil.

The wafer to target space is preferably about 140 mm but can range from about 1.5" to 6". For this wafer to target spacing, a coil diameter of about 11 1/2 inches is preferred. Increasing the diameter of the coil which moves the coil away from the workpiece edge can have an adverse effect on bottom coverage. On the other hand, decreasing the coil diameter to move the coil closer to the wafer edge can adversely affect layer uniformity. It is believed that decreasing the coil diameter will cause the coil to be more closely aligned with the target

resulting in substantial deposition of material from the target onto the coil which in turn can adversely affect the uniformity of material being sputtered from the coil.

As set forth above, the relative amounts of material sputtered from the target 110 and the coil-shield 104 are a function of the ratio of the RF power applied to the coil and the DC power applied to the target. However, it is recognized that in some applications, an RF power level which is optimum for improving the uniformity of the deposited layer of materials from the coil and the target may not be optimum for generating a plasma density for ionization. An alternative embodiment of a plasma chamber may have a second target which, although generally shaped like a coil, is not coupled to an RF generator. Instead, the second target may be formed of a flat closed ring coupled through feedthrough standoffs to a variable negative DC bias source. As a consequence, the chamber may have three "targets," the first target 110, the second ring target, and the RF coil-shield 104.

A variety of precursor gases may be utilized to generate the plasma including Ar, H₂ or reactive gases such as NF₃, CF₄ and many others. Various precursor gas pressures are suitable including pressures of 0.1 - 50 mTorr. For ionized PVD, a pressure between 10 and 100 mTorr is preferred for best ionization of sputtered material.

It will, of course, be understood that modifications of the present invention, in its various aspects, will be apparent to those skilled in the art, some being apparent only after study others being matters of routine mechanical and electronic design. Other embodiments are also possible, their specific designs depending upon the particular application. As such, the scope of the invention should not be limited by the particular embodiments herein described but should be defined only by the appended claims and equivalents thereof.

40 Claims

1. An apparatus for coupling energy from a source into a plasma, the apparatus comprising:
 - a target
 - a substrate holder;
 - a plasma generation area between said target and said substrate holder;
 - a conductive first shield wall defining a channel so that said first shield wall further defines a plurality of spaced coil windings, said conductive first shield coil windings being coupled to said source to radiate energy from said conductive first shield coil windings into said plasma generation area.
2. The apparatus of claim 1 wherein said channel occupies less than 10% of the surface area of said

- conductive first shield wall.
3. The apparatus of claim 1 wherein said channel is less than 1/4 inch in width so that said conductive first shield windings are spaced less than 1/4 inch apart. 5
 4. The apparatus of claim 1 wherein said conductive first shield wall is generally cylindrical in shape. 10
 5. The apparatus of claim 4 wherein said conductive first shield wall channel encircles said conductive first shield wall a plurality of times to define said conductive first shield wall windings. 15
 6. The apparatus of claim 4 wherein said conductive first shield wall channel is helix-shaped.
 7. The apparatus of claim 1 wherein said plasma generation area has a lateral face and said first shield wall has a surface area sufficient to enclose at least 50% of said plasma generation area lateral face. 20
 8. The apparatus of claim 1 further comprising a second shield wall positioned outside said conductive first shield wall. 25
 9. The apparatus of claim 8 further comprising insulating standoffs between said first and second shield walls 30
 10. An apparatus for coupling electromagnetic energy from an RF source into a plasma, the apparatus comprising: 35
 - a chamber having a chamber wall;
 - a conductive first shield wall positioned within the chamber to protect at least a portion of the chamber wall from deposition materials, wherein said shield wall comprises a continuous strip of conductive material which forms a multi-turn coil having two ends, one end of which is coupled to said first RF source and the other end is coupled to ground. 40
 11. The apparatus of claim 10 further comprising a second shield wall positioned between said conductive first shield wall and said chamber wall to protect at least a portion of the chamber wall from deposition materials. 50
 12. The apparatus of claim 11 further comprising insulating standoffs between said first and second shield walls.
 13. The apparatus of claim 10 comprising a channel separating each turn of said first conductive shield wall multi-turn coil, said channel occupying less than 10% of the surface area of said conductive first shield wall! 55
 14. The apparatus of claim 13 wherein said channel is less than 1/4 inch in width so that said conductive first shield turns are spaced less than 1/4 inch apart.
 15. The apparatus of claim 10 wherein said conductive first shield wall is generally cylindrical in shape.
 16. The apparatus of claim 15 further comprising a channel separating each turn of said first conductive shield wall multi-turn coil wherein said conductive first shield wall channel encircles said conductive first shield wall a plurality of times to define said conductive first shield wall turns.
 17. The apparatus of claim 16 wherein said conductive first shield wall channel is helix-shaped.
 18. The apparatus of claim 10 wherein said plasma generation area has a lateral face and said first shield wall has a surface area sufficient to enclose at least 50% of said plasma generation area lateral face.
 19. A method of forming an RF antenna for a plasma deposition chamber, comprising:
 - cutting a channel into a shield wall of conductive material so that said shield wall forms a multi-turn coil;
 - installing said shield wall between a target and a substrate holder; and
 - coupling a source of RF energy to said shield wall.
 20. The method of claim 19 wherein said cutting is performed using a laser.
 21. The method of claim 19 wherein said cutting is performed using a hot wire.
 22. The method of claim 19 wherein said conductive first shield wall is generally cylindrical in shape.
 23. The method of claim 22 wherein said channel encircles said conductive first shield wall a plurality of times to define said conductive first shield wall turns of said coil.
 24. The method of claim 19 wherein said channel is cut into a helix shape.
 25. The method of claim 19 wherein said channel is less than 1/4 inch wide.
 26. The method of claim 19 further comprising blocking

deposition material using a second shield wall positioned behind said first shield wall.

27. A method of sputter depositing a deposition material onto a substrate, comprising: 5
- coupling RF energy into a plasma from a first conductive shield wall formed into a coil having a plurality of windings; and
ionizing deposition material passing through 10
said plasma.
28. The method of claim 27 wherein said conductive first shield wall is generally cylindrical in shape. 15
29. The method of claim 28 wherein said conductive first shield wall has a channel which encircles said conductive first shield wall a plurality of times to define said conductive first shield wall turns of said coil 20
30. The method of claim 29 wherein said channel is cut into a helix shape.
31. The method of claim 29 wherein said channel is less 25
than 1/4 inch wide
32. The method of claim 27 further comprising blocking deposition material using a second shield wall positioned behind said first shield wall. 30

35

40

45

50

55

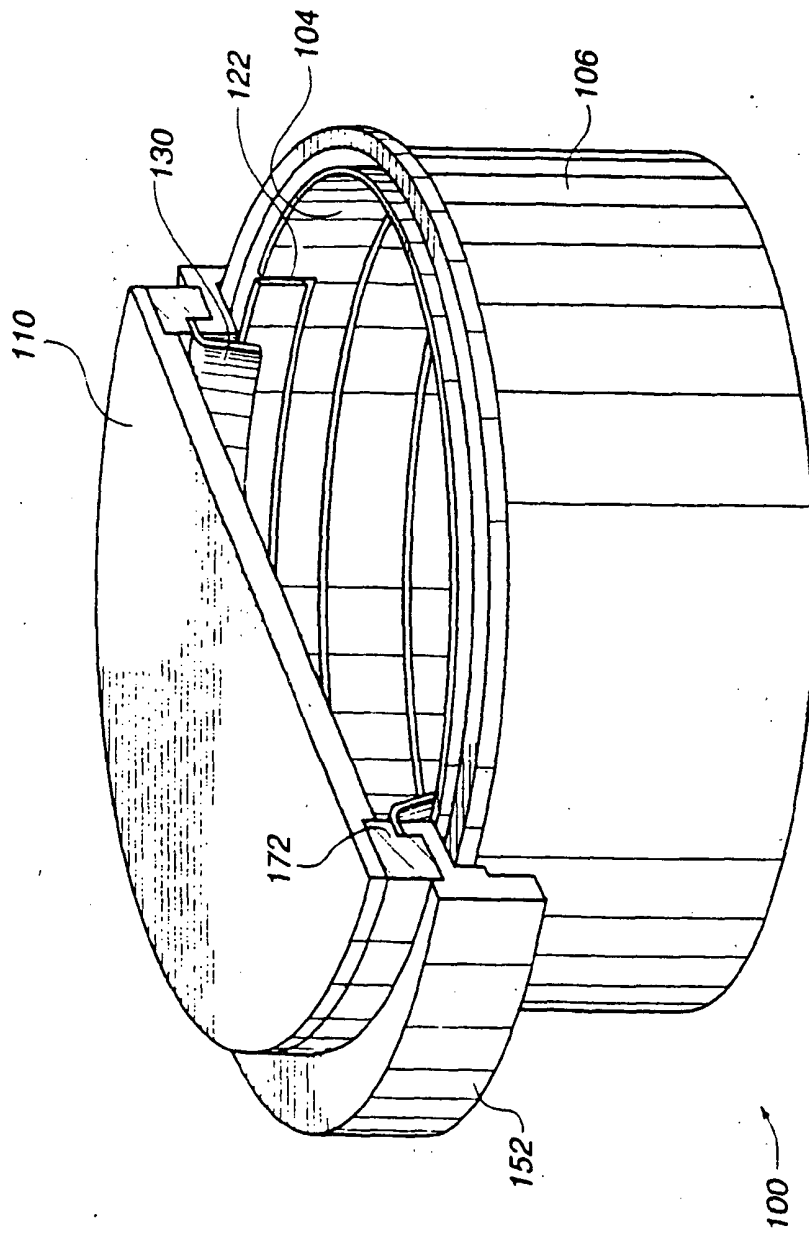


Fig. 1

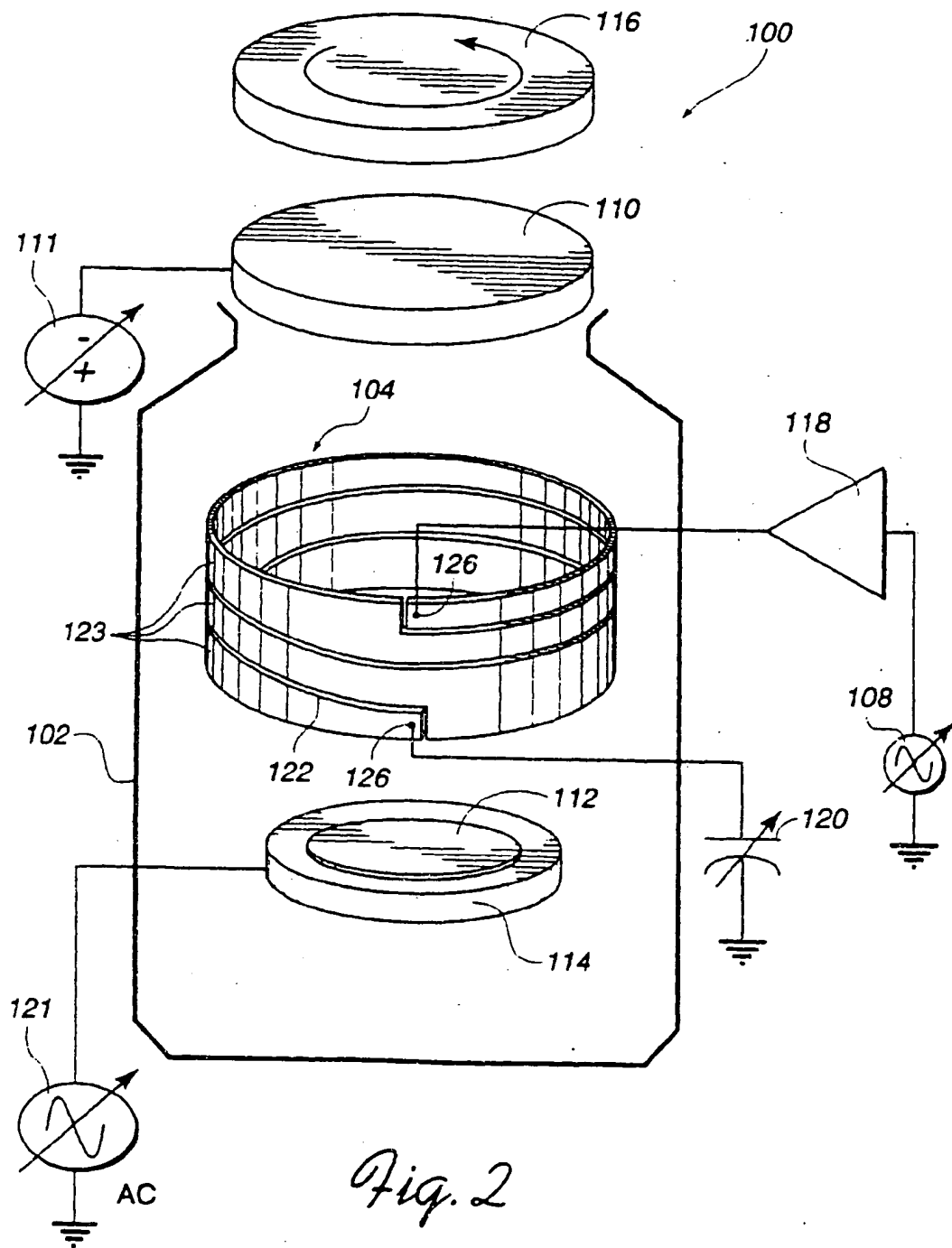
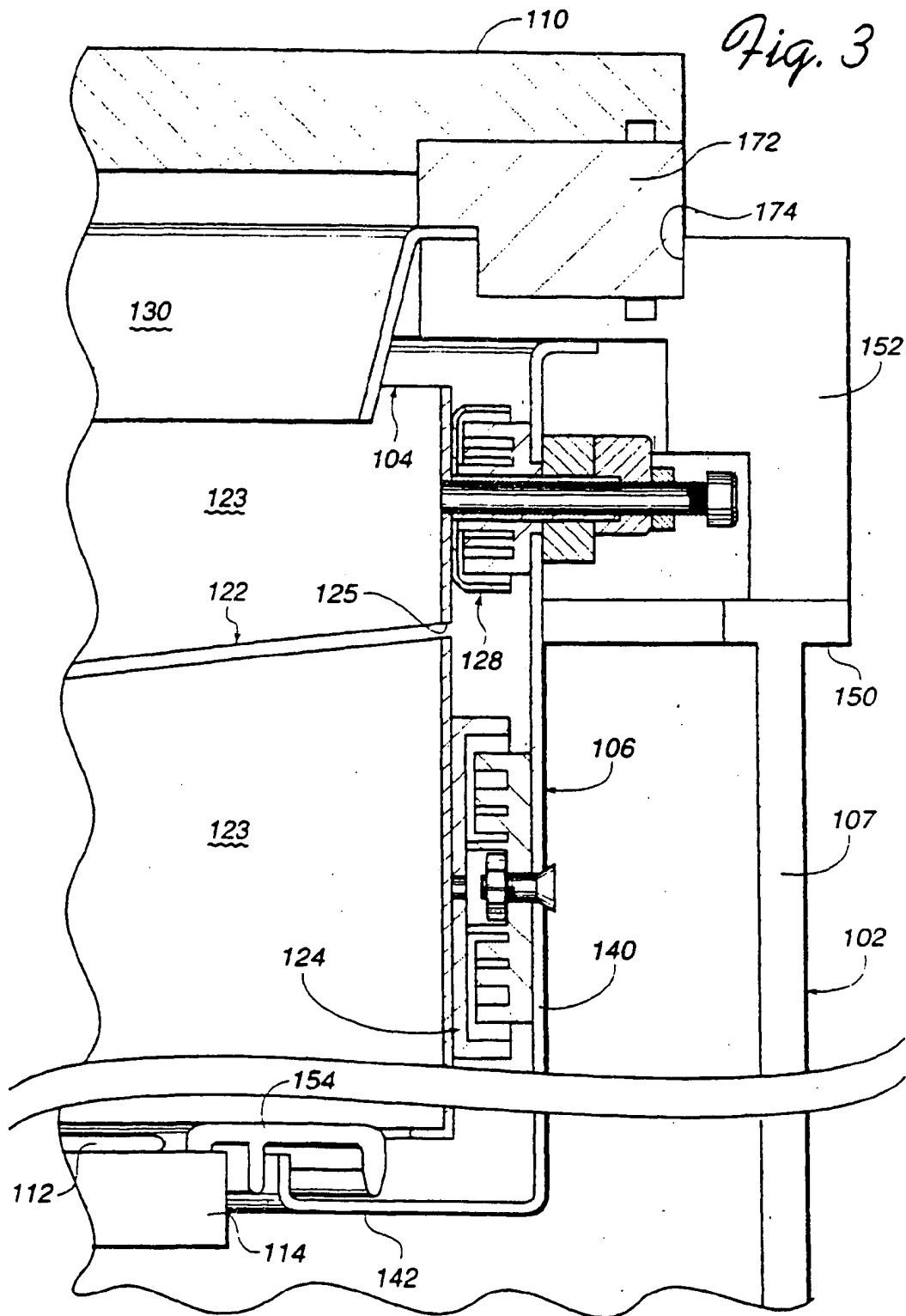


Fig. 2



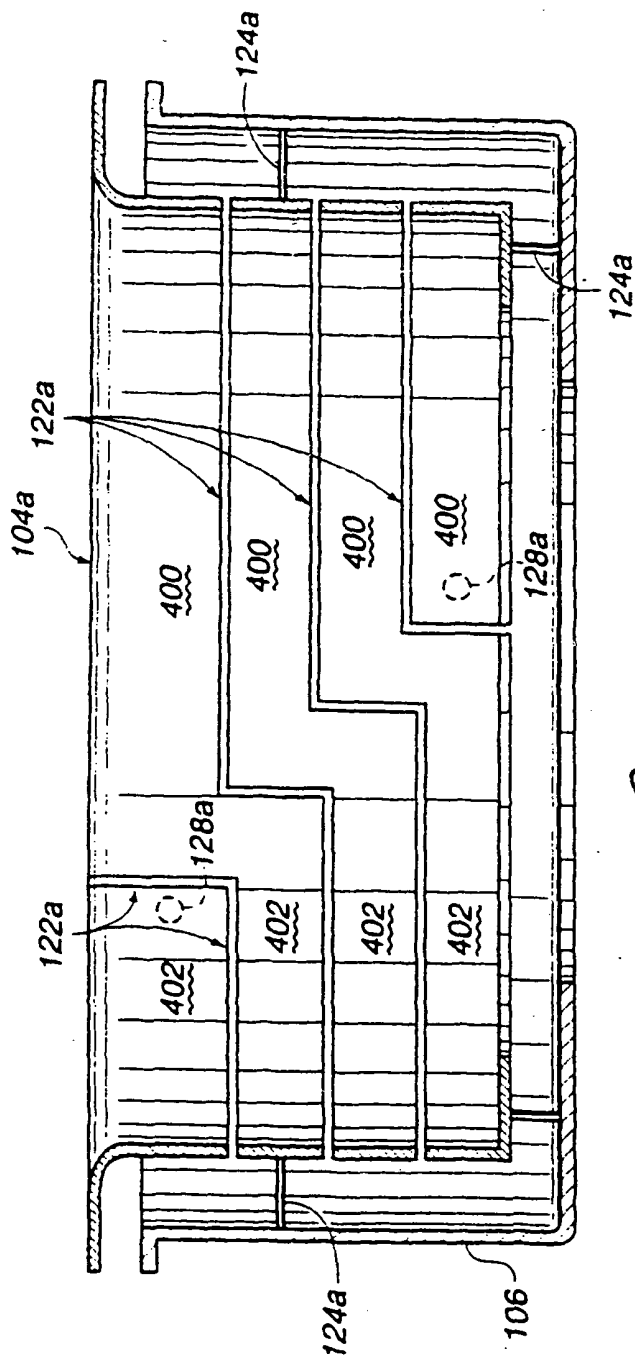


Fig. 4



Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) EP 0 836 218 A3

(12) EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
16.09.1998 Bulletin 1998/38

(51) Int Cl.⁶ H01J 37/34, H01J 37/32

(43) Date of publication A2:
15.04.1998 Bulletin 1998/16

(21) Application number: 97307864.5

(22) Date of filing: 06.10.1997

(84) Designated Contracting States:
AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC
NL PT SE
Designated Extension States:
AL LT LV RO SI

(72) Inventors:
• Edelstein, Sergio
Los Gatos, California 95030 (US)
• Subramani, Mani
San Jose, California 95135 (US)

(30) Priority: 08.10.1996 US 730722

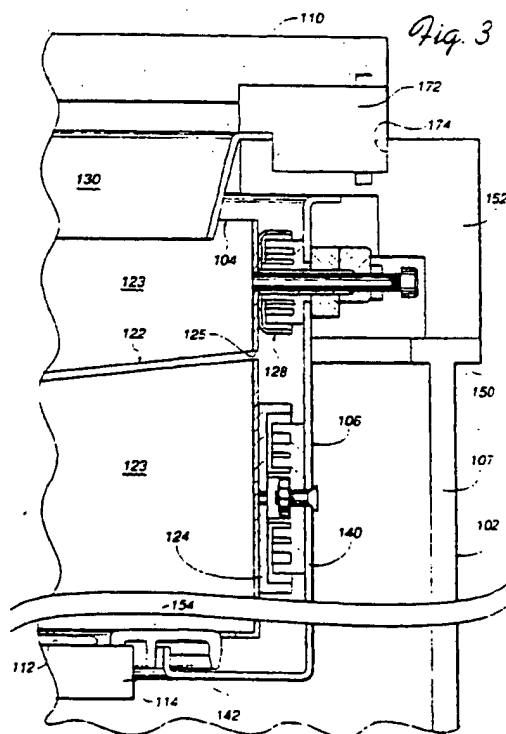
(74) Representative: Bayliss, Geoffrey Cyril et al
BOULT WADE TENNANT,
27 Furnival Street
London EC4A 1PQ (GB)

(71) Applicant: APPLIED MATERIALS, INC.
Santa Clara, California 95054-3299 (US)

(54) Active shield for generating a plasma for sputtering

(57) A combination coil and shield (104) for a plasma chamber (100) in a semiconductor fabrication system is provided. The coil-shield has a plurality of turns

to couple energy efficiently into a plasma and also substantially blocks deposition material from reaching a second shield (106) positioned behind the first shield



EP 0 836 218 A3

EUROPEAN SEARCH REPORT

EP 97 30 7864

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate of relevant passages	Relevant in claim:	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
D,P. X	EP 0 774 886 A (APPLIED MATERIALS INC) 21 May 1997	1,4-6, 10-12, 15-17, 27-30	H01J37/34 H01J37/32
P,Y	* page 5, line 33 - page 6, line 50 * * page 7, line 49 - page 8, line 46; figures 3,6,7 *	19.20	
Y	US 4 568 845 A (UEHARA KENJI) 4 February 1986 * column 3, line 11 - line 26; figure 3 *	19.20	
A	EP 0 593 924 A (LEYBOLD AG) 27 April 1994 * column 2, line 35 - column 3, line 40; figure 1 *	1,10,27	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			H01J
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 17 July 1998	Examiner: Schaub, G
CATEGORY OF CITED DOCUMENTS		T: theory or principle underlying the invention E: earlier patent document but published on or after the filing date D: document cited in the application I: document cited for other reasons A: technological background G: non-written disclosure P: intermediate document	
X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category		A: member of the same patent family, corresponding document	